

Need to then compile a .rcp file with pilrc  
(for form info)

```
pilrc hello.rcp
```

produces  
a .ro  
file of  
several  
.bin files

Finally, put all the components together

```
build-prc -o hello.prc -t appl -c LFhe  
-n "Hello" *.src *.bin
```

## Makefiles

File consisting of rules of the form

```
target: dep1 dep2
```

↓  
command

↑  
tag

Ex)

```
hello: hello.c
```

```
      m68k-palmos-gcc -c hello.c -o hello.o
```

Ex)

```
clean: rm hello.o hello
```

To cause ~~make~~ <sup>run</sup> make to run at command prompt

```
> make
```

prompt

← evaluates 1st target in file

```
> make hello.o
```

← tries to satisfy ~~hello.o~~ target

## Variables

Can have lines like

```
var = value
```

Obj = hello.o hi.o

Touch:

hello : \$(OBJ)
m68k-palmos-gcc \$(OBJ)

Patterns considers

%.o : %.c

bind corresponding thing ending w/ .c

then execute command

m68k-palmos-gcc -c \$< -o \$@

fill in

fill in target

\$\* - matches hello.o

Other command \ continue next line

ls ; \
cd ..

for each thing that ends in .o

ex) hello.o

Example of hello makefile in book